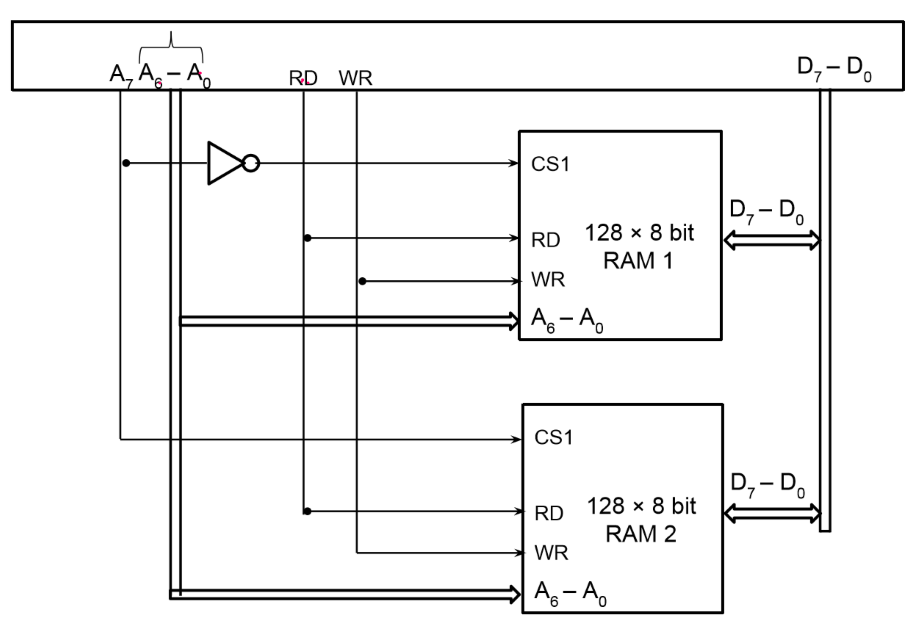
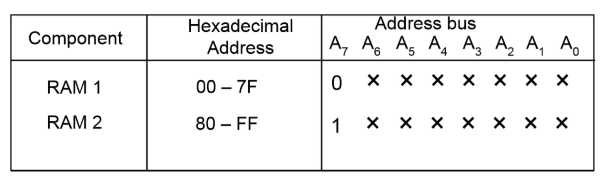
**Problem Statement 11**: Design a 256B RAM chip using 128B RAM chip .

**Circuit diagram:**

****

**Memory Address Map:**

****

**Output:**

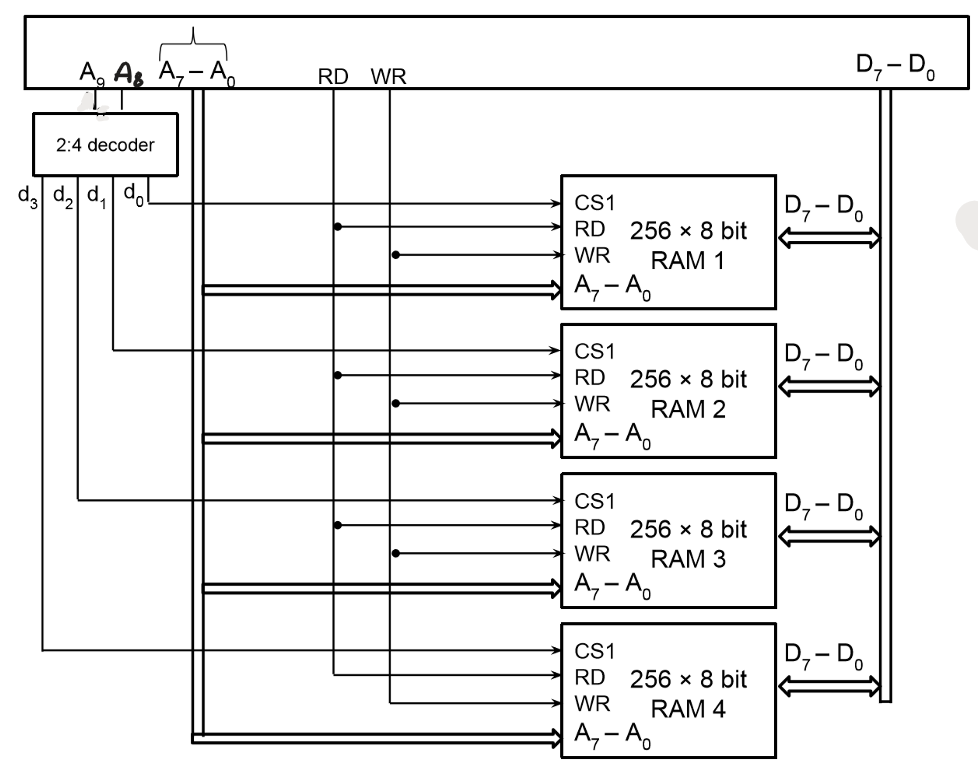


**A diagram of a computer chip

Description automatically generated**

**Problem Statement 12**: Design a 1kB RAM chip using 256B RAM chip.

**Circuit diagram:**

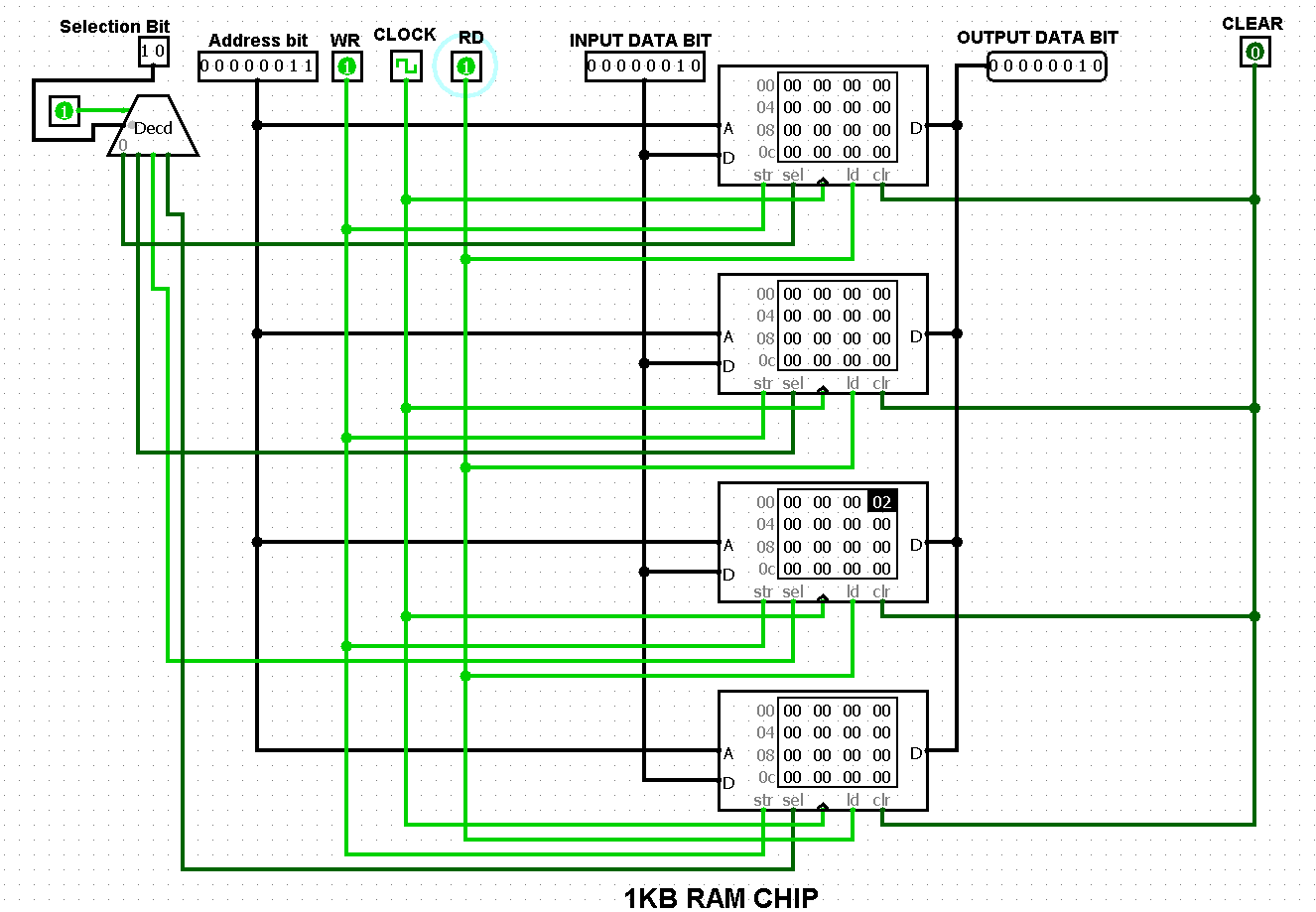
****

**Memory Address Map:**

A table with numbers and letters

Description automatically generated

**Output:**



**Problem Statement 13**: Design a 1KB memory chip using 128B RAM chip and 512B ROM chip.

**Circuit diagram:**

A diagram of a computer

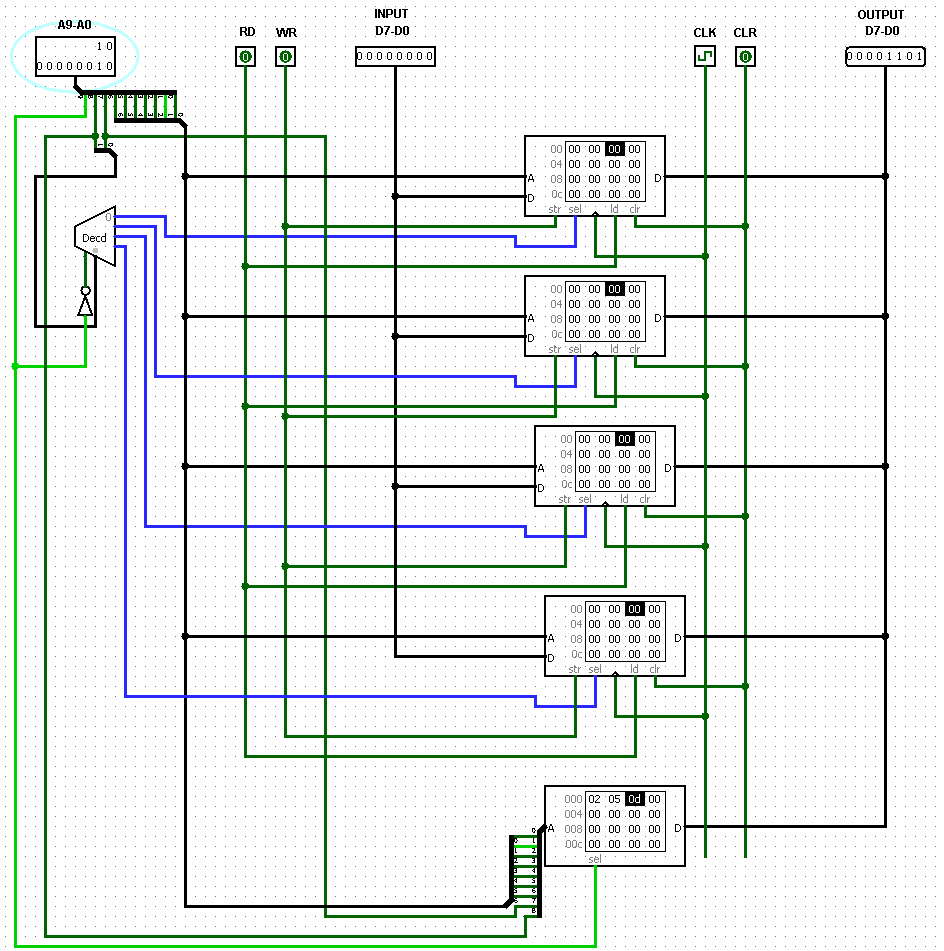
Description automatically generated

**Memory Address Map:**

A table with numbers and letters

Description automatically generated

**Output:**



**1KB memory chip using 128B RAM chip and 512B ROM chip**

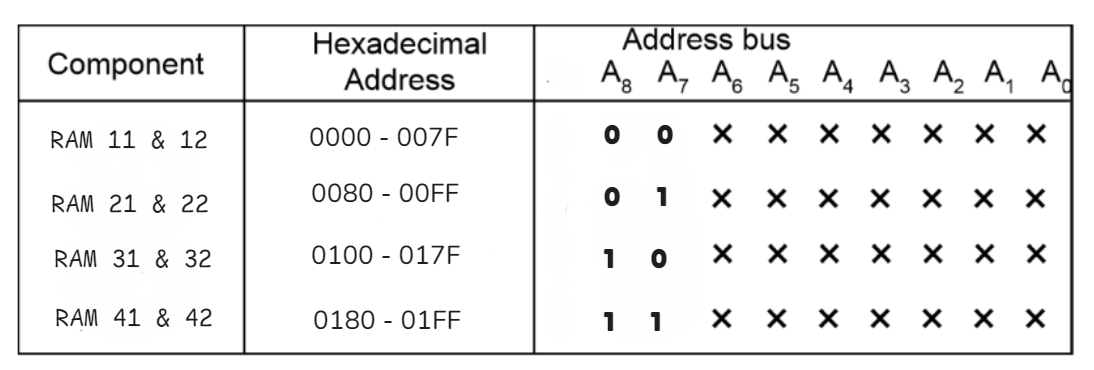
**Problem Statement 14**: Design a 512 X 16 RAM chip using 128 X 8 RAM chip.

**Circuit diagram:**

A diagram of a computer

Description automatically generated

**Memory Address Map:**



**Output:**

A diagram of a computer

Description automatically generated

**512 X 16 RAM chip using 128 X 8 RAM chip**

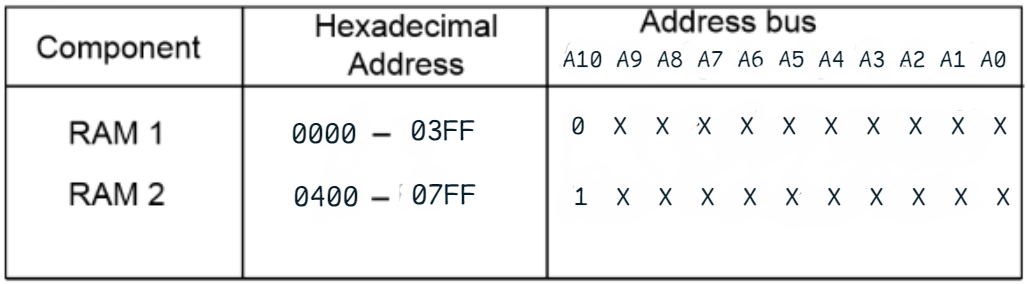
**Problem Statement 15:** Design a 2KB RAM chip by using above mention 1KB RAM chip.

**Circuit diagram:**

A diagram of a computer

Description automatically generated

**Memory Address Map:**



A diagram of a computer

Description automatically generated with medium confidence

**Block Diagram of 1KB RAM chip**

**Output:**

A diagram of a machine

Description automatically generated

**2KB RAM chip by using above mention 1KB RAM chip**